

MULTI-PORT NETWORK INTERFACE CIRCUIT AND RELATED METHOD FOR SCRAMBLING CODES OF DIFFERENT PORTS WITH DIFFERENT SEEDS AND RESETTING SIGNAL TRANSMISSION OF DIFFERENT PORTS AT DIFFERENT TIMES

Abstract

A multi-port network interface circuit and related control method. The network interface circuit has a plurality of PHY circuits, each of the PHY circuit is used for transmitting signals to a plurality of corresponding network nodes via different ports. Each of the PHY circuits scrambles signals transmitted at different ports with different seeds, and the network interface circuit resets different PHY circuits at different times such that each of the PHY circuits starts to transmit signals at a different time.